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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicants: Yujiro KAJIHARA *et alii*

Serial No.: 09/328,910

Filed: 9 June 1999

For: LEAD FRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, USING THE SAME, AND METHOD OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2851

Examiner: J. Clark

REQUEST FOR CORRECTED OFFICE ACTION AND RESTART OF THE PERIOD FOR RESPONSE

Honorable Commissioner for Patents
Washington, D.C. 20231

19 April 2001

Sir:

A first Office Action on the merits was mailed 11 April 2001 in connection with the above-identified application.

Upon review of the Action, Applicants found that the Notice of References Cited Form PTO-892 indicated on the Action Summary was not attached. However, copies of references not previously cited in the application accompanied the Action. Therefore, Applicants submit that Form PTO-892 was not supplied with Applicants' copy of the Action, as required by 37 C.F.R. §1.104(d)(1).

Accordingly, Applicants respectfully request a corrected Office Action be mailed. Applicants further respectfully request that the shortened statutory period for response be restarted in the corrected Action.

This Request is being filed within one month of the mailing date of the Action, and as set forth in the MPEP, is therefore timely submitted for the requested actions.

No fee is required for the filing of this Request. However, if any fee is actually appropriate for this submission, please charge such fee to ATS&K Deposit Account No. 01-2135 (as Order No. 501.32049R00), and credit any overpayment or excess fee thereto.

Respectfully submitted,

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